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*Happy  
Teacher's Day*

**HAPPY  
ENGINEER'S  
DAY**



**KITS**

**KKR & KSR Institute of Technology & Sciences**

*(Approved by AICTE New Delhi, Affiliated to JNTU Kakinada, Accredited by NAAC with "A" Grade)*



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## VISION, MISSION & PEO'S

### Vision

Developing highly Qualitative, Technically Competent and Socially Responsible Engineers.

### Mission

To provide quality education in the domain of Electronics and Communication Engineering through

- Enriched curriculum for addressing the needs of Industry.
- Effective teaching learning processes through congenial environment.
- Gaining contemporary knowledge through research, development, curricular, co and extra-curricular.

### ECE Program Educational Objectives

Graduates of Electronics & Communication Engineering Shall

**PEO1:** Develop a strong background in basic science and mathematics and ability to use these tools in their chosen fields of specialization.

**PEO2:** Have the ability to demonstrate technical competence in the fields of electronics and communication engineering and develop solutions to the problems.

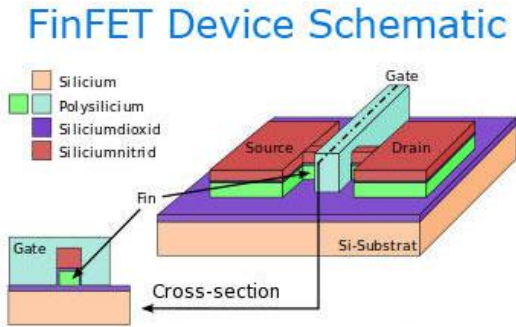
**PEO3:** Attain professional competence through life-long learning such as advanced degrees, professional registration, and other professional activities.

**PEO4:** Function effectively in a multi-disciplinary environment and individually, within a global, societal, and environmental context.

**PEO5:** Take individual responsibility and to work as a part of a team towards the fulfillment of both individual and organizational goals.

The institute is a symbol of egalitarian outlook without discretions. KITS student activity council is organized exclusively by students with representatives from various disciplines stands for the advocacy of democracy and leadership opportunities provided by the institute. KITS student clubs enable all the students and staff mingle freely to express their views and share their talents and expertise. **KITS imparts Outcome Based Education (OBE)** which gives equal opportunities to teaching and learning curricular, co-curricular and extra-curricular activities.

# FIN FIELD EFFECT TRANSISTOR



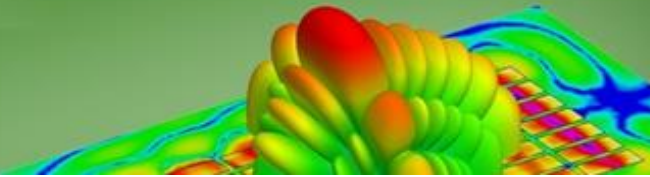
**FinFET**, also known as **Fin Field Effect Transistor**, is a type of non-planar or "3D" transistor used in the design of modern processors. As in earlier, planar designs, it is built on an SOI (silicon on insulator) substrate. However, FinFET

designs also use a conducting channel that rises above the level of the insulator, creating a thin silicon structure, shaped like a fin, which is called a **gate electrode**. This fin-shaped electrode allows multiple gates to operate on a single transistor.

This type of multi-gate process extends **Moore's law**, allowing semiconductor manufacturers to create CPUs and memory modules that are smaller, perform faster, and consume less energy. Intel began releasing FinFET CPU technology in 2012 with its 22-nm Ivy Bridge processors.

FINFET ADVANTAGES	
PARAMETER	DETAILS
Power	Much lower power consumption allows high integration levels. Early adopters reported 150% improvements.
Operating voltage	FinFETs operate at a lower voltage as a result of their lower threshold voltage.
Feature sizes	Possible to pass through the 20nm barrier previously thought as an end point.
Static leakage current	Typically reduced by up to 90%
Operating speed	Often in excess of 30% faster than the non-FinFET versions.

By/-E.V.Narayana , Asst. Prof



SPACE the Association of Department of Electronics and communication engineering, KKR & KSR Institute of Technology and science, Guntur has organized a two day workshop on “**Antenna modeling techniques using HFSS tool**” on 31<sup>st</sup> August to 1<sup>st</sup> September & 6<sup>th</sup> September to 7<sup>th</sup> September 2018. The resource person for this work shop is “**Mr.M.Shekar , Asst,Prof Vignana University**” . This workshop is conducted to enhance the design idea of antennas using software tool .

HFSS is high frequency structure simulator it is high performance full wave electromagnetic field simulator 3D volumetric passive device modeling that takes advantages of familiar Microsoft Windows graphical user interface .it integrates simulation, visualization ,solid modeling and automaton in easy to learn environment. HFSS contains multiple simulation engines in one package, each targeted toward a specific application or simulation output  
In the workshop he explained the following things

- About Antenna
- Parameters of designing an antenna
- Micro Strip antenna
- Designing of Antenna using HFSS Tool
- COAXIAL feeding



At the end of the session the speaker was honored by the HOD sir with a Shalv and Memento. After the feedback from the participants, The Vote of Thanks was delivered by **Ms. K. Leela Rani**, Assistant Professor of ECE

# Happy Teacher's Day



We celebrate Teachers' day every year throughout the country on 5<sup>th</sup> September. Students express their gratitude and appreciation for their teachers on this day. Students take the total responsibility of this event.

A teacher is a friend, philosopher, and guide who holds our hand, opens our mind, and touches our heart. The contribution of a teacher cannot be ignored at all. In India, the Teacher's

Day is celebrated on 5th September and this tradition started from 1962. This is the when **Dr. Sarvepalli Radhakrishnan** was born. He was a philosopher, scholar, teacher, and politician and his dedicated work towards education made his birthday an important day in the history of India

At 3.30 Pm all the faculty members were assembled at Class room. The program started with the speech of a student. He said "Teachers play a major role in making their students responsible citizens of tomorrow and good human beings. It is impossible to imagine our lives without teachers. The relationship between a teacher and a student cannot always be put into words. Later Dr. Ch. Aruna Bala madam and Mr. K.Mallikarjun , Asst. prof express their view with the students. The event ended with words of wisdom by the Dr. Siva Ganga Prasad, HOD ECE and the in-charges.





The ECE department association "SPACE" celebrated Engineer's Day on 15<sup>th</sup> September 2018. Engineers' Day is celebrated on September 15<sup>th</sup> every year in India commemorating the birthday of one of the greatest engineers in India so far and a great human being Sir **Mokshagundam Visvesvarayya**, popularly known as **Sir MV**... In this occasion, The SPACE had conducted Technical Elocution, Circuit Mania and Technical Quiz for ECE students.

Sir Mokshagundam Vishweshvaraya KCIE, FASc popularly known as Sir MV; 15 September 1861 – 12 April 1962) was an Indian engineer, scholar, statesman, and the 19th Diwan of Mysore, who served from 1912 to 1918. He received India's highest honour, the Bharat Ratna, in 1955. He was knighted as a Knight Commander of the British Indian Empire (KCIE) by King George V for his contributions to the public good. 15 September is celebrated as Engineer's Day in India in his memory. During technical elocution one of the student said that One participant explained that the basic aim of Electronics and Communication Engineers is to produce products that are smaller, smarter and multi-functional. According to another participant Electronics engineer must find new solutions to the practical problems affecting our daily lives.



On 15<sup>th</sup> September 2018 prizes and certificates distributed to all the winners and runners of Technical Elocution, Circuit Mania and Technical Quiz.

# STUDENT ACHIEVEMENTS

## Technical Quiz Winners List:

S.No	Name	Position
1	15JR1A0409 B.SOWMYA	WINNERS
	16JR1A04G8 V.KRISHNA KALYAN	WINNERS
	17JR1A0464 K.JAHNAVI	WINNERS
2	15JR1A0450 D.ANAND ROY	RUNNERS
	17JR5A0405 N.B.BABU SUBHANI	RUNNERS
	17JR1A0437 A.SAI CHAITANYA	RUNNERS

## Circuit Mania Winners List:

S.NO	ROLL NUMBERS	NAME OF THE STUDENT	PRIZE
1	17JR1A04A0	G.BRAHMATEJA	<b>FIRST</b>
	17JR1A04A1	K.VAMSI KRISHNA	
	17JR1A04A4	K.GIDEON	
2	17JR1A04B4	M.PAVAN YOGESH	<b>SECOND</b>
	17JR1A04B6	M.DHEERAJ KRISHNA	
	17JR1A04B9	M.BARNABA	

## Technical Elocution Winners List:

S.No	Name	Position
1	17JR1A04G1 P.V.N.BHARDADWAJ	<b>WINNER</b>
2	17JR1A04H4 T.R.SAI SURAJ	<b>RUNNER</b>





# 41

The Training & Placement Cell of the College has proudly announce the placement of 41 students of the 2015- 2019 batch Electronics and Communication Engineering students batch placed in the ON CAMPUS DRIVE conducted on 21<sup>st</sup> and 22<sup>nd</sup> September 2018..

The Management, Principal, Faculty and Staff Members wholeheartedly Congratulate and Wishes the Placed Students and Placement Coordinator & his team for their effort in placing the students.



### Capgemini Selected list

Sr. No.	Roll No	Name
1	15JR1A04C0	Panadi Akhila
2	15JR1A0485	M.Lakshmi Lasya
3	15JR1A0451	Devayojanam Krishna Srinivas
4	15JR1A04E7	Vemula Bhargavi
5	15JR1A0452	Doddi Hari Naga Viswanath
6	15JR1A04D8	Shilpa Mary.Santhiyagu

7	15JR1A04C7	Punyamurthy Durga Lakshmi Vasudha
8	15JR1A04D9	Surepalli Maneesha Rani
9	15JR1A0468	K. Rechal Praisy
10	15JR1A0410	Bandlamudi Sai Anjani
11	15JR1A0462	Jorige Sri Lakshmi Sravani
12	15JR1A0403	Akuthota Lakshmi Pravallika
13	15JR1A0411	Pravallika Bayyapu
14	16JR5A0406	Kudumula Matha Bhavani
15	15JR1A0413	Bellam Radhika
16	15JR1A0476	Prameela
17	15JR1A04E3	Vajja Sai Lakshmi Mythili
18	15JR1A0453	Duggi Sreeramasai
19	15JR1A0414	Rolla Poojitha
20	15JR1A04A7	Kurapati Venkata Sai Teja
21	15JR1A0407	Atukuri.Mounika
22	15JR1A04C5	Pottimutyam.Geethalakshmi
23	15JR1A0418	Cheemakurty Kiranmayi
24	15JR1A0448	Chinthala Gopi
25	15JR1A04E2	Turaka Padmini
26	15JR1A0488	Nagabothu. Ganga Bhavani
27	15JR1A0473	Koya Lakshmi Prasanna
28	15JR1A04E5	Veerisetti Lavanya
29	15JR1A0474	Lella Archana Bhavani
30	15JR1A0427	Gangavarapu Sindhu
31	15JR1A0449	Saikumar Dalaraju
32	15JR1A04C6	Pranamika Dora
33	15JR1A0483	Jaya Mounika Mugadasu
34	15JR1A0414	Bolla Pooja Reddy
35	15JR1A0406	Annavaajula Krishna Teja Sree

36	15JR1A0420	Tejaswi Chintala
37	15JR1A0475	Lingamallu. Ynv Divya Supriya
38	15JR1A0419	Srimadhumitha Chimakurthy
39	15JR1A0431	Geetha Gummadi
40	15JR1A04F3	Paladugu Darwin Chowdary
41	15JR1A0425	Divvela Sai Yamini

Capgemini understands that business value cannot be achieved through technology alone. It starts with people: experts working together to get to the heart of your individual business objectives and develop the most adapted solutions to fit these requirements. We believe this human-centered approach to technology is what makes the difference for your business.

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We are happy to announce that our ECE student ***P.Darwin Chowdary 15JR1A04F3*** placed in a core company VEDA IIT company.

The Management, Principal, Faculty and Staff Members wholeheartedly Congratulate and Wishes the P.Darwin Chowdary

EDA IIT (VLSI Engineering and Design Automation) is an industry driven state-of-the-art training institute of excellence in various fields such as VLSI and Embedded systems. Since its inception in 1997, 150 batches have passed out and around a total of 3500 students have graduated. The curriculum of all the courses at VEDA, is set in such a manner that the student has more hands on experience in his chosen field. VEDA is associated with a consortium of VLSI Design Houses for their participation in designing and structuring the curricula and also for their active involvement in imparting Industry Oriented Training on contemporary VLSI Design.



# FACULTY ACHIEVEMENTS

	<p><b>Mrs. K.Sowjanya, Asst. Prof</b> got member ship of <i>"The society of Digital Information and wireless Communication (SDIWC)"</i> with membership ID: 1587</p>	
	<p><b>Ms. N.Soniya, Asst. Prof</b> got member ship of <i>"The society of Digital Information and wireless Communication (SDIWC)"</i> with membership ID: 1585</p>	
	<p><b>Ms. Leela Rani, Asst. Prof</b> got member ship of <i>"The society of Digital Information and wireless Communication (SDIWC)"</i> with membership ID: 22047</p>	

## BODY LANGUAGE IN CAMPUS PLACEMENTS

Body language must not be confused with sign language, as sign languages are full languages like spoken languages and have their own complex grammar systems, as well as being able to exhibit the fundamental properties that exist in all languages. Body language, on the other hand, does not have a grammar system and must be interpreted broadly, instead of having an absolute meaning corresponding with a certain movement, so it is not a language like sign language, and is simply termed as a "language" due to popular culture.

Right body language and formal dress code always get plus points for you. You are going to be tested not only for what you know about your area of expertise but also on how you present yourself at the interview.

Meet the interviewers warmly. It's good and appreciable if you offer a strong handshake to them. Strong handshake shows warmth and enthusiasm. Experts advise candidates to look like a mature person. Being a little imperfect is not bad. After all, nobody is perfect. Smile on your face reflects a charming personality.

**By/-Sai Anjani (IV ECE-I)**