KITS

KKR & KSR INSTITUTE OF TECHNOLOGY & SCIENCES

(Approved by AICTE, Delhi, Affiliated to JNTU, Kakinada)

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Date: 15/06/2019

Circular

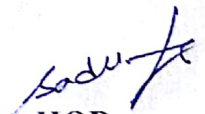All the students are here by informed that,

The basic aim of education is to bring about the harmonious and integrated growth of every child so that he/she is able to meet all challenges. Thus, the department has decided to hold remedial classes in order to equip the students with all necessary skills. These classes aim at developing the potential of each child to the optimum by giving him/her individual help and attention. It is mandatory for students whose performance falls below satisfactory levels to attend the remedial classes.

The classes will commence from Monday, 17th June 2019. The classes will be held from 8.10 AM to 9.40 A.M for III Year Students and from 3.50 PM to 5.20 PM for II & IV year students.

The schedule for the remedial classes is as follows:

Day	II Years (3.50 PM to 5.20 PM)	III Year (8.10 AM to 9.40 AM)	IV Years (3.50 PM to 5.20 PM)
Monday	Electronic Devices and Circuits	Computer Architecture and Organization	Radar Systems
Tuesday	Switching Theory and Logic Design	Linear I C Applications	Digital Image Processing
Wednesday	Signals and Systems	Digital I C Applications	Computer Networks
Thursday	Network Analysis	Digital Communications	Optical Communications
Friday	Random Variables and Stochastic Process	Antenna and Wave Propagation	Electronic Switching Systems
Saturday	Managerial Economics & Financial Analysis	--	Embedded Systems

Faculty I/C

HOD