

**KKR&KSR Institute of Technology and Sciences  
Vinjanampadu, Guntur, Andhra Pradesh-522017**

Approved by AICTE, New Delhi and Permanent Affiliation from JNTUK, Kakinada  
Accredited with "A" Grade by NAAC & NBA Accreditation Status for 4 UG (CSE, ECE, EEE, ME) Programs

**MHRD- IIC Report Format**

1. Objective of the Event: To enrich the Technical Skills of the Students
2. About the Program/Event: Four different events (Paper presentation, project expo, technical quiz and electronthon) are conducted and the prizes are distributed to the top three.
3. Details of External Participants (If any): NIL
4. Details of Resource Persons : Dr. Ramesh Vaddi, Assoc. Professor, SRM University, Amaravathi, AP, India,
5. Venue of the Event: Seminar Hall, KKR&KSR Institute of Technology & Sciences, Guntur
6. Date & Time of the Event: 12/09/2019 & 10:00 am
7. No of Students participated: 150
8. Year, Branch and Section of Students: II & III year, ECE and sec-1, 2 &3
9. No of Faculties participated: NIL
10. One Student Member Feed Back:
11. Promotion of the Event on the Social Media Website: (Link and Screenshot)
12. Promotion of the Event on the University/college Website :( Link and Screenshot)
13. Event Photographs from different angles covering all the students, Banner and speaker (Include 4 or 6 photographs in the Document and send those photos
14. 1- 2 minutes video of the event (Drive Link Only)
15. Benefit in terms of learning/Skill/Knowledge obtained \*: (Not Less than 1000 words)
16. Expenditure Amount ( If any) : 26,000/-
17. Remarks:
18. Experiences and Output of the Session

**Note: Sample student feedback is enclosed for reference.**

**Details of 4 Photographs from different angles covering all Students and Faculty**



### **Experiences and Output of the Session:**

Along with students Faculty members attended the space day celebrations on 12/09/2019 conducted by ECE department, KKR & KSR Institute of Technology and Sciences, Guntur. The main aim of the space day is to explore ongoing research in the relevant fields. The space day aims to bring together innovative academicians, researchers and industry to join hands for research development. A guest lecture on current technologies related to VLSI Design, delivered by Dr. V. Ramesh from SRM University, Amravati, Guntur.

### **Student Feedback/ Outcomes of the Workshop:**

#### **Outcomes of the Session**

I am Shaik Mastan Vali, pursuing III year SEC-3 in KKR & KSR Institute of Technology and Sciences with ECE discipline. I have attended the guest lecture, along with space day celebrations. First of all I want to thank my college management who provided such facility for all of us to learn and experience about all the technical sessions. In this regard my sincere thanks to HOD, who have been conducting such type of events for the student nourishment. We attended the guest lecture program 12<sup>th</sup> September, 2019 by **Dr. V. Ramesh from SRM University, Amravati, and Guntur**. The session is Very useful and informative. The seminar mainly aimed to create awareness on VLSI Design challenges. On behalf of total KITS students community my heartfelt thanks to Space faculty coordinators for organizing such events. He clearly explained CMOS technology, operation and its applications. Once again thank you very much.

## **Resource Person**



**Name:**Dr. Ramesh Vaddi

**Designation:**Associate Professor

**Department:**ECE

**School/University:**SEAS, SRM University, Amaravati, AP

**Email:**[Ramesh.V@srmmap.edu.in](mailto:Ramesh.V@srmmap.edu.in)

## **Executive Summary**

Dr. Ramesh Vaddi completed Ph.D. in Microelectronics and VLSI from Indian Institute of Technology Roorkee, India in 2011. He was a Senior Research Fellow at National University of Singapore during 2018-19, Postdoctoral Scholar at Pennsylvania State University, USA during 2013 and Postdoctoral Research Fellow in the School of Electrical and Electronic Engineering, Nanyang Technological University during, 2011 -2012. Dr.Vaddi has worked as an Assistant Professor in IIIT Naya Raipur and Shiv Nadar University, India and presently he is an Associate Professor in ECE Department at SRM University, Amaravati, AP. His research interests include Post-CMOS devices for energy efficient VLSI Designs, Hardware security for IoT, VLSI accelerators for AI Edge Computing. At Penstate, he was involved in the Semiconductor Research Corporation (SRC) sponsored projects “Centre for Low Energy Systems Technology (LEAST) and NSF sponsored Nanosystems Engineering Research Centre for Advanced Self-Powered Systems of Integrated Sensors and Technologies (ASSIST).

He has published research outcomes in several top IEEE conferences and peer reviewed International journals in the device and circuits society and has about 45 research publications and 2 USA patents to his credit. He held few visiting researcher appointments and invited talks at various globally reputed institutes such as Hong Kong University of Science and Technology and Missouri University of Science and Technology, Rolla, USA. Dr. Ramesh Vaddi has been a TPC member of various International Conferences/Symposiums such as VDAT, INIS and a reviewer of IEEE Trans. on BioCAS, IEEE Trans. on VLSI systems, IEEE Tran. On Electron Devices, IEEE Tran. On Electron Devices Letters, IEEE Tran. On Nanotechnology, Elsevier Microelectronics Journal, Journal of Applied Physics, IOP Semiconductor Science and Technology, etc, and Technical committee member of Several IEEE International conferences such as DAC, ISCAS, ISLPED, etc.