

ABOUT KITS

KKR & KSR INSTITUTE OF TECHNOLOGY & SCIENCES (KITS) was established in the year 2008, by GSR & KKR EDUCATIONAL SOCIETY in Vinjanampadu village, Vatticherukuru Mandal of Guntur district by Sri. Koye Subba Rao. The Institution was started with four AICTE approved undergraduate programmes in Departments of Electrical and Electronics Engineering (EEE), Electronics and Communications Engineering (ECE), Computer Science and Engineering (CSE), Information Technology (IT) with an overall intake of 240 students. Later programmes in MBA, Departments of Civil Engineering and Mechanical Engineering were inducted. This year (2015-16), the college has grown manifold with a total student intake of 906 pursuing courses in 5 undergraduate and 5 postgraduate programmes. The vision of the Institute is to impart quality education through exploration and experimentation and generate socially-conscious engineers, embedding ethics and values, for the advancement in Science and Technology, though the affiliating University introduces curriculum based on the feedback of industry, students and faculty members, the Institute assesses the existing curriculum and adds extra topics beyond the syllabus. Electives are offered by the affiliating University at both post graduate and under graduate levels in terms of specializations. All programmes offered by the Institute are semester based. The intake of programmes approved by AICTE and affiliated to JNTUK Kakinada. The institution is accredited by NAAC with A grade.

ABOUT THE ECE DEPARTMENT

The Department of Electronics and Communication Engineering was started in the year 2008. Since then, the department has grown in manifold and has become one of the important departments in the college. The department offers a four year B. Tech degree programme and a two year M. Tech Degree programme. The department maintains a close liaison with industries and ensures that students are trained as per the current needs of the society. The curriculum provides in depth knowledge of both hardware and software skills. The department is full-fledged with adequate infrastructure and state-of-art laboratories.

ABOUT THE WORKSHOP

This programme is targeted to Electronic, Electrical, and Mechatronic Engineers with some knowledge on Digital Signal Processing Concepts and Microprocessors. This course is based on the Texas Instruments' TMS320C6713 processor and its implementation using Code Composer Studio (CCS).

This programme provides emphasis on programming aspects of TMS320C6713 processor using CCS.

Thus it offers various lab experiments to implement several DSP algorithms using C and Assembly Language Programming.

NOTE: The number of seats are strictly restricted to 40, filled on first come first serve basis.

STEERING COMMITTEE

Chief Patron:

Sri. Koye SubbaRao, Chairman

Patrons:

Sri. Koye Sekhar, Secretary

Dr. P. Babu, Principal

Workshop Chair:

Dr. M. Siva Ganga Prasad., Professor & HoD of ECE

Organizing Chair:

Dr. Md. Zia Ur Rahman, Professor of ECE

Organizing Co-Chair:

Mr. K. Murali Krishna, Asst. Professor of ECE

Organizing Committee:

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Mr.Sarath Kumar, Assistant Professor of ECE

Mrs.T.Bhavani, Assistant Professor of ECE

Mr.M.Srinivas, Assistant Professor of ECE

Mr.E.V. Narayana, Assistant Professor of ECE

Mrs.P.Sarala, Assistant Professor of ECE

Mr.K.Mallikarjuna Rao, Assistant Professor of ECE

Mrs.Tirumala Devi, Assistant Professor of ECE

Mr. E.Bhanu Prasad, Assistant Professor of ECE

Mr. Ch. ChandraSrkhhar Reddy, Assistant Professor of ECE

Mr.P.Ashok Babu, Assistant Professor of ECE

Mr.M.Sudhir, Assistant Professor of ECE

Mr.T.Venkat, Assistant Professor of ECE

Miss.T.Revathi, Assistant Professor of ECE

Mr.B.Venu, Assistant Professor of ECE

Mr.M.Nagaraju, Assistant Professor of ECE

Ms.Indira Priyadarsini, Assistant Professor of ECE

Mr.Kumara Swamy, Assistant Professor of ECE

Mr.M.Rakesh, Assistant Professor of ECE

A Three Day National Workshop

on

Real Time DSP Implementation on

TMS320C6713 DSK

8th -10th June 2017

APPLICATION FORM

1. Name:

2. Qualification :

3. Designation :

4. Mailing Address:

Pin code:

E-mail:

Contact Number:

5. REGISTRATION FEE Details:

Amount: Rs. 2000/- Date:

Signature of the Applicant



ABOUT THE RESOURCE PERSON

Our resource person Mr. U. Madhu Sudhan Rao, M.Tech (IIT, Delhi) is Director at UG Consultants, Bangalore, India. Mr Rao is a full time training consultant with specialization in DSP. He has 8 years of professional experience in universities, 6 years of industry experience and more than 3 years of professional training experience. He has launched UG Consultants in April 2008 at Bangalore and is quite actively associated with both industry and academia by conducting corporate training programs, faculty training programs and workshops on DSP concepts and on DSP Processors.

As a DSP and Embedded System Engineer in the industry he was involved in complete product development cycle. He has built and managed advanced training programs on aspects of DSP technology for the development of real time DSP based Embedded Systems for a number of organizations.

As a DSP training consultant, he has provided corporate training programs at many top industry organizations and R&D in India and Singapore. He is also working as a training consultant in the field of DSP for many organizations.

Mr.Rao has provided corporate training programs at India for Centre for Development of Advanced Computing (CDAC), Bharat Electronics Limited (BEL), Indian Air Force (IAF), Tata Elxsi, Kongsberg and Alcatel Lucent and Ashok Leyland etc.

Also he has provided training programs at Singapore for DSO National Laboratories, Institute of Technical Education (ITE West), DHI Water & Environment Pte Ltd.

At UG Consultants he has guided under graduate and post graduate students in their DSP based academic projects. He is also a consultant for many technological institutions.

Mr.Rao received Master's degree from Indian Institute of Technology, New Delhi in 1994 with specialization in Digital Signal Processing.

Speech Enhancement, Automatic Speech Recognition, Acoustic Echo Cancellation, Audio Post Processing, Image Enhancement and Restoration are the fields of his interest.

WHO WILL BENEFIT FROM THIS COURSE

This course is specially designed for *Electronics, Electrical, and Mechatronic professionals, Professional Executives working in the DSP based industry, Academic Faculty Members, Research Scholars and Graduated Engineers* interested to

- ✦ design and develop DSP based embedded systems,
- ✦ understanding the VLIW architecture and
- ✦ interested in TI Processors and its programming processors.

Prerequisites:

- ✦ basic knowledge of SCILAB and C programming
- ✦ basic knowledge of DSP algorithms
- ✦ basic knowledge of Microprocessors

COURSE CONTENTS

Day 1

DSP Algorithms and Architecture,
CCS and Data Formats and
Arithmetic
Fixed Point C Implementation
Implementation of DSP Algorithms, Profiling, Double
Precision arithmetic & related lab practice.

Day 2

CPU Architecture of TMS320C6713,
Pipeline and Instruction Set
ASM and Mixed C and ASM Programming
Linear Assembly Programming & related lab practice.

Day 3

EDMA, Peripheral Controllers, Interrupt System
DSP BIOS
Optimization (Software Pipelining)
Lab experiments based on polling and interrupts, DSP BIOS,
Optimization procedures & related lab practice.

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Organized by

IEEE Student branch,

Department of

Electronics and Communication Engineering



KKR & KSR INSTITUTE OF TECHNOLOGY & SCIENCES

(Approved by AICTE, Delhi, Affiliated to JNTU, Kakinada)

Accredited by NAAC with 'A' Grade)

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