



**KKR & KSR INSTITUTE OF TECHNOLOGY AND SCIENCES**  
 (Approved by AICTE, New Delhi, Affiliated to JNTUK Kakinada, Accredited with 'A' grade by NAAC)  
**Electronics and Communication Engineering Department**  
Academic Year 2022-2023  
**CLASS TIME TABLE**

Class: II-B.Tech ECE-I

Semester: 2

LH.N0:- 213

w.e.f:18-01-2023

Period /Day	1	2	3	11:00-12:00	4	5	6
	08:00-09:00	09:00-10:00	10:00-11:00		12:00-01:00	01:00-02:00	02:00-03:00
Monday	CS	CRT	DS	LUNCH	EMTL	AC	EMTL
Tuesday	Data Structures Lab				CS	EMTL	AC
Wednesday	ECA	CRT	EMTL		LIB	AC	DS
Thursday	CS	ECA	AC		SOC	EMTL	DS
Friday	ECA/AC LAB				ECA	SOC	DS
Saturday	AC/ECA LAB				ECA	CS	S & G

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
20EC4T01	Electronic Circuit Analysis	Dr.K. Madhusudana Rao	20EC4L01	Electronic Circuit Analysis Lab	V.Rajani/SD.Vaheeda
20EC4T02	Analog Communications	B.Vijayakumari	20EC4L02	Analog Communications Lab	B.Vijayakumari /J.Sairam
20EC4T03	Electromagnetic Waves & Transmission Lines	Dr.N.Adinarayana	20CS4L04	Data structures Lab	G.Srujana/ P.Aravind
20EE4T02	Control Systems	M.Rajanaik	20EC4S01	Basic Level Skill Oriented Course II	J.Sairam
20CS4T04	Data structures	G.Srujana		LIB	B.Vijayakumari
				S & G	B.Vijayakumari

Class In-charge  
(B.Vijayakumari)

Time Table Co-Ordinator  
(G.Madhusudhana Rao)

Head Of The Department  
(Dr.N.Adinarayana)



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**Electronics and Communication Engineering Department**  
Academic Year 2022-2023  
**CLASS TIME TABLE**

Class: II-B.Tech ECE-II

Semester: 2

LH.N0:-214

w.e.f: 18-01-2023

Period /Day	1	2	3	11:00-12:00	4	5	6
	08:00-09:00	09:00-10:00	10:00-11:00		12:00-01:00	01:00-02:00	02:00-03:00
Monday	ECA/AC LAB			LUNCH	CS	ECA	DS
Tuesday	AC	CRT	EMTL		DS	SOC	ECA
Wednesday	Data Structures Lab				EMTL	LIB	CS
Thursday	ECA	CRT	AC		AC/ECA LAB		
Friday	CS	AC	DS		SOC	EMTL	<b>ECA</b>
Saturday	DS	EMTL	CS		AC	ECA	S & G

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
20EC4T01	Electronic Circuit Analysis	Dr.K. Madhusudana Rao	20EC4L01	Electronic Circuit Analysis Lab	Dr.K. Madhusudana Rao/T.Bhavani
20EC4T02	Analog Communications	K.Sowjanya	20EC4L02	Analog Communications Lab	K.Sowjanya/ B.Vijayakumari
20EC4T03	Electromagnetic Waves & Transmission Lines	Dr.N.Adinarayana	20CS4L04	Data structures Lab	P.Aravind/ G.Srujana/
20EE4T02	Control Systems	M.Rajanaik	20EC4S01	Basic Level Skill Oriented Course II	J.Sairam
20CS4T04	Data structures	G.Srujana		LIB	J.Sairam
				S & G	K.Sowjanya

**Class In-charge**  
(K.Sowjaya)

**Time Table Co-Ordinator**  
(G.Madhusudhana Rao)

**Head Of The Department**  
(Dr.N.Adinarayana)



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**Electronics and Communication Engineering Department**  
**Academic Year 2022-2023**  
**CLASS TIME TABLE**

**Class: II-B.Tech ECE-III**

**Semester: 2**

**LH.N0: 218**

**w.e.f: 18-01-2023**

Period /Day	1	2	3	11:00-12:00	4	5	6
	08:00-09:00	09:00-10:00	10:00-11:00		12:00-01:00	01:00-02:00	02:00-03:00
Monday	ECA	SOC	DS	LUNCH	ECA/AC LAB		
Tuesday	EMTL	CS	AC		AC/ECA LAB		
Wednesday	ECA	AC	EMTL		LIB	DS	EMTL
Thursday	Data Structures Lab				DS	CS	ECA
Friday	CS	CRT	AC		EMTL	DS	SOC
Saturday	AC	CRT	EMTL		ECA	CS	S & G

Course Code	Course Name	Name of the Faculty	Course Code	Course Name	Name of the Faculty
20EC4T01	Electronic Circuit Analysis	V.Rajani	20EC4L01	Electronic Circuit Analysis Lab	T.Bhavani/V.Rajani
20EC4T02	Analog Communications	K.Sowjanya	20EC4L02	Analog Communications Lab	K.Sowjanya /J.suneetha
20EC4T03	Electromagnetic Waves & Transmission Lines	T.Bhavani	20CS4L04	Data structures Lab	G.Srujana/ P.Aravind
20EE4T02	Control Systems	J.Anand	20EC4S01	Basic Level Skill Oriented Course II	J.Sairam
20CS4T04	Data structures	P.Aravind		LIB	T.Bhavani
				S & G	V.Rajani

**Class In-charge**  
**(V.Rajani)**

**Time Table Co-Ordinator**  
**(G.Madhusudhana Rao)**

**Head Of The Department**  
**(Dr.N.Adinarayana)**