



(Approved by AICTE, New Delhi, Affiliated to JNTU Kakinada)

Guest Lecturer Report

DATE : 11th October 2017

TIME : 11.00 AM TO 12.30 PM

VENUE: Seminar Hall, KITS Guntur

ORGANIZED BY : E.C.E Dept.

FACULTY INCHARGE: Mr. M.Sudhir & Mr.P.Ashok



EVENT DESCRIPTION:

The ECE association *SPACE* conducted a “**Guest Lecturer**” on “**ASIC Design and Testability**” on 11th, **October, 2017** from 11.00 AM to 12.30 PM in the Seminar Hall. **Mr.Avinash Yadlapalli** is resource person for this lecture. This lecture is conducted to get the awareness among the students towards ASIC Design.

SESSION ACTIVITIES:

The Department of E.C.E has made proper arrangements for transportation for the guest to the campus. As per the given instructions by the **Dr. Siva Ganga Prasad, HOD ECE dept.**, the faculty incharges **Mr.P.Ashok** and **Mr. M.Sudhir** made proper arrangements for this Lecturer. The program started with the opening remarks of HOD. One of the ECE final year students gave the brief introduction about the guest. Exactly at 11.30 AM the lecture is started by **Mr.Avinash Yadlapalli**. The Resource person given hands of experience on VLSI and ASIC Design.

He explained “An ASIC (application-specific integrated circuit) is a microchip designed for a special application, such as a particular kind of transmission protocol or a hand-held computer. You might contrast it with general integrated circuits, such as the microprocessor and the random access memory chips in your PC.”

Main objective of this guest lecture was to provide, Awareness to the students with the Industry level ASIC and FPGA design flow, ASIC Design Flow using Industry standard tools Explain the various steps involved in ASIC design flow, Analyze the role of FPGA Design and test bench in functional simulation environment, Steps of Designing Application for FPGA.

He explained

- What is Semiconductor?
- What is an ASIC

- Front end Design Flow
- Functional Simulation
- HDL synthesis
- Design implementation
- Testing Basics

He gave the brief information about the VLSI industries present in INDIA like ntel, Nvidia, Qualcomm, Broadcom -> Now Avago, Texas Instruments (TI), AMD, ARM..... etc. He shared his email and Contact number with students for any help. Finally Guest is honored by the Dr. Siva Ganga Prasad, HOD ECE dept.

Photos:

